

Editorial: Special Issue on High-Frequency-Link Power-Conversion Systems, 2014

RECENT advances in solid-state semiconductors, magnetic and capacitive materials, and microelectronics technologies, coupled with the growing need for high power density, low footprint space, and reduced weight, without compromising efficiency, cost, and reliability, has provided the impetus for high-frequency-link power-conversion systems. While such a need has been everlasting in power electronics, new technologies and sciences have fueled the impetus for newer growth areas that transcend conventional applications. The potential demand for such high-frequency-link power-conversion systems is growing leaps and bounds encompassing renewable and alternative energy systems, power grid, smart appliances, telecommunication, power quality, compact magnetics, and electrical-transportation applications to name only a few. A closer scrutiny of the high-frequency-link power-conversion technology and its wide application point to the need for a broader overview as well as deeper understanding of the technologies that lay the foundation for the continual growth of this emerging power electronics domain.

The overall objective of this IEEE Power Electronics Society (PELS) Special Issue on high-frequency-link power-conversion systems is, therefore, to bring out the highlights of the ongoing world-wide activities in this area of advanced research to expose, analyze, and resolve the critical research and developmental challenges. The Special Issue received a total of 132 manuscripts for review considerations of which 35 manuscripts have been accepted for final publication. These publications encompass all four broad categories of high-frequency-link power-conversion systems (i.e., dc/ac, ac/dc, ac/ac, and dc/dc converters) with broad applicability in the following areas of application: photovoltaic energy, wind energy, electric vehicles, fuel-cell energy, energy storage, uninterruptible power systems, motor drives, high-frequency-ac power distribution systems, high-voltage-direct-current transmission, active load emulator, smart/micro grid, solid-state transformers, induction heating, railway traction drive, pulsed power, plug-in hybrid-electric-vehicle, battery charging, inductive power transfer. The contributions of the articles compiled in this special issue are multifold encompassing topologies with plurality of stages and/or levels, modulation, loss-mitigating switching, high-frequency magnetics, control, modeling, switched-capacitor power conversion, power-factor correction, and broad overview of the Special-Issue subject area.

Of course, without the overwhelming contributions of the authors this Special Issue would not have materialized. Nor, would it have seen the light of the day without the voluntary and time-bound services of the numerous reviewers who on plurality of occasions provided multitude of insightful and constructive

feedbacks. We express our indebtedness to both of these societal representatives.

Thanks are also due to all of the Guest Associate Editors to whom we owe our sincerest gratitude for their tireless services notwithstanding their own busy professional life to ensure the efficacy and sanctity of the review process. The following Guest Associate Editors have contributed to the establishment of this special issue: I. Barbi (University Federal de Santa Catarina, Florianópolis, Brazil), P. J. Wolfs (Central Queensland University, Rockhampton, Australia), B. Ferreira (Delft University of Technology, Delft, Netherlands), L. Chang (University of New Brunswick, Fredericton, NB, Canada), A. M. Trzynadlowski (University of Nevada, Reno, NV, USA), R. W. De Doncker (RWTH Aachen University, Aachen, Germany), R. Burgos (Virginia Polytechnic Institute and State University, Blacksburg, VA, USA), A. Kawamura (Yokohama National University, Yokohama, Japan), I. Batarseh (University of Central Florida, Orlando, FL, USA), G. Holmes (RMIT, Melbourne, Australia), S. Bhattacharya (North Carolina State University, Raleigh, NC, USA), P. Enjeti (Texas A&M University, College Station, TX, USA), T. Shimizu (Tokyo Metropolitan University, Tokyo, Japan), B. Ozpineci (Oak Ridge National Laboratory, Oak Ridge, TN, USA), A. Agarwal (Department of Energy, Washington, DC, USA), K. Mino (Fuji Electric, Tokyo, Japan), Y. Xue (Siemens, Princeton, NJ, USA), L. Casey (Saton, San Jose, CA, USA), R. Raju (General Electric, Fairfield, CT, USA), P. M. Barbosa (Delta, Taipei, Taiwan), P. Tenca (General Electric, Munich, Germany), J. Leach (Kyma Technologies, Raleigh, NC, USA), S. Leslie (Powerex, Harrison, OH, USA), B. S. Jacobson (Raytheon, Waltham, MA, USA), P. Friedrichs (Infineon, Neubiberg, Germany), and R. M. Schupbach (Cree, Durham, NC, USA).

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SUDIP K. MAZUMDER, *Guest Editor-in-Chief*

JOHANN W. KOLAR, *Guest Editor*

HIROFUMI AKAGI, *Guest Editor*

DEHONG XU, *Guest Editor*



Sudip K. Mazumder (S'97–M'01–SM'03) received the Ph.D. degree in electrical and computer engineering from Virginia Tech, Blacksburg, VA, USA, in 2001.

He is currently a Professor in the Department of Electrical and Computer Engineering, University of Illinois, Chicago (UIC), IL, USA. He has 22 years of professional experience and has held R&D and design positions in leading industrial organizations and served as a consultant for several industries. He also serves as the President of NextWatt LLC since 2008. Since joining UIC, he has been awarded about 40 sponsored projects by leading U.S. federal research organizations and industries. He has published more than 160 journal and conference papers and nine book/book chapters, presented more than 50 invited presentations, and holds seven issued and several pending patents.

Dr. Mazumder received several prestigious awards including University of Illinois's University Scholar Award, IEEE PELS Transaction Prize Paper Award, IEEE Future Energy Challenge Award, ONR Young Investigator Award, and NSF CAREER Award. He serves as the Guest Editor-in-Chief and as an Associate Editor of multiple IEEE Transactions and was the first Editor-in-Chief for *Advances in Power Electronics*. He has served and is serving in high-profile capacities on leading PELS Conference TPCs, PELS TCs, IEEE Working Group, and high-profile NSF panels.



Johann W. Kolar (S'89–M'91–SM'04–F'10) is a Full Professor in power electronics at the Swiss Federal Institute of Technology (ETH), Zurich, Switzerland, and the Chair of the ETH Power Electronic Systems Laboratory. He has proposed numerous novel converter topologies and modulation/control concepts, e.g., the VIENNA Rectifier, the Swiss Rectifier, and the three-phase ac–ac sparse matrix converter. In this context, he has published more than 550 scientific papers in international journals and at main international conferences, and two book chapters. In addition, he has filed more than 110 patents. The focus of his current research is on ac–ac and ac–dc converter topologies with low effects on the mains, solid-state transformers for smart microgrids, ultra-compact and ultra-efficient converter modules employing latest power semiconductor technology (SiC and GaN), power supply-on-chip systems, multiobjective optimization, and ultrahigh speed and bearingless motors.

Dr. Kolar is a Fellow of the IEEE and received 17 IEEE Transactions or Conference Prize Paper Awards. He initiated and/or is the founder/co-founder of four ETH Spin-off companies. He is a Member of the Steering Committees of several leading international conferences in the field and has been serving as an Associate Editor of the IEEE TRANSACTIONS ON INDUSTRIAL ELECTRONICS and IEEE TRANSACTIONS POWER ELECTRONICS.



Hirofumi Akagi (M'87–SM'94–F'96) received the Ph.D. degree in electrical engineering from the Tokyo Institute of Technology, Yokohama, Japan, in 1979.

He is currently a Professor and Chair in the Department of Electrical and Electronic Engineering, Tokyo Institute of Technology. His research interests include power conversion systems and their applications to industry, transportation, and utility. He has authored and coauthored more than 110 IEEE Transactions papers and two invited papers published in Proceedings of the IEEE in 2001 and 2004.

Dr. Akagi received six IEEE Transactions Prize Paper Awards. He is the recipient of the 2001 IEEE Power Electronics Society William E. Newell Award, the 2004 IEEE Industry Applications Society Outstanding Achievement Award, the 2008 IEEE Richard H. Kaufmann Technical Field Award, and the 2012 IEEE Power & Energy Society Nari Hingorani Custom Power Award. He was an IEEE PELS and IAS Distinguished Lecturer from January 1998 to December 1999. He served as the President of the IEEE Power Electronics Society from January 2007 to December 2008 for two years. Since January 2014, he has been serving as the IEEE Division II Director-Elect.



Dehong Xu (M'94–SM'09–F'13) received the B.S., M.S., and Ph.D. degrees from the Department of Electrical Engineering, Zhejiang University, Hangzhou, China, in 1983, 1986, and 1989, respectively.

Since 1996, he has been a Full Professor in the College of Electrical Engineering, Zhejiang University. He is also the Director of the Power Electronics Institute, Zhejiang University. His research interests include power electronics topology and control, power conversion for energy saving, and renewable energy. He has authored five books and more than 150 IEEE journal and conference papers. He holds more than 20 Chinese patents and three US patents.

Dr. Xu is the President of the China Power Supply Society. He was at-large Adcom Member of the IEEE Power Electronics Society from 2006 to 2008. He is an Associate Editor of the IEEE TRANSACTIONS ON POWER ELECTRONICS and IEEE TRANSACTIONS ON SUSTAINABLE ENERGY. He was the General Chair of the IEEE International Symposium on Industrial Electronics, Hangzhou, China, 2012, and the General Chair of the IEEE International Symposium on Power

Electronics for Distributed Generation Systems, Rogers, AR, USA, 2013. He has received three IEEE conference paper awards.