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## High Efficiency Drive System with 3-Level T-Type Inverter

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# High efficiency drive system with 3-level T-type inverter

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## Keywords

<<Multilevel converters>>, <<Efficiency>>, <<Electrical drive>>, <<Induction motor>>, <<Converter machine interactions>>.

## Abstract

In this paper an efficiency optimized variable speed drive is presented. It consists of the alternative 3-level T-type converter topology which is very efficient for low switching frequencies and a standard induction machine. The total system efficiency is optimized concerning the converter losses as well as fundamental and harmonic induction machine losses.

## 1 Introduction

Variable speed drives for low-voltage applications are traditionally built with 2-level inverters because of the tremendous cost pressure. Although it was shown previously that 3-level topologies offer various advantages in terms of output voltage quality, harmonic losses in the machine, isolation stress and overvoltages due to long motor cables, they have never achieved a large market penetration. The main reasons for the small acceptance are the increased costs due to the additional semiconductors, the additional gate drive units and the potentially lower reliability.

To overcome the mentioned drawbacks and still benefit from the superior 3-level voltage waveform, the alternative 3-level T-type converter (3LT<sup>2</sup>C) [1–4] is considered for low-voltage variable speed drive applications. There, the conventional 2-level VSC topology is extended with an active, bidirectional switch to the DC-link mid-point as depicted in Fig. 2.

The 3LT<sup>2</sup>C has several advantages compared with alternative 3-level topologies such as the well known 3-level NPC [5] or the recently presented 3-level split inductor converter [6]. Contrary to the NPC topology, the 3LT<sup>2</sup>C is very efficient for low switching frequencies due to low conduction losses and needs only three additional isolated gate drive power supplies compared to the 2-level converter. Differently, the split inductor converter is able to generate 3-level voltage outputs without additional semiconductors and gate drives, but it employs bulky and expensive bridge leg inductors which also produce losses.

The 3LT<sup>2</sup>C basically combines the advantages of the 2-level converter such as low conduction losses and small part count with the advantages of the 3-level converter such as low switching losses and superior output voltage quality.

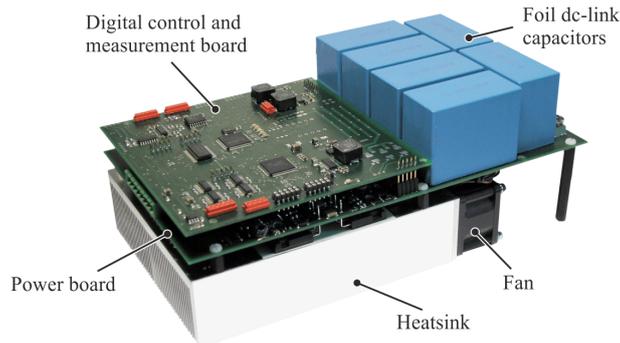


Figure 1: Prototype of the 3LT<sup>2</sup>C.

If the total drive efficiency is considered, the main share of the losses occurs in the induction machine. Several efficiency optimal machine control algorithms have been proposed in the past such as search algorithms [7] and model-based efficiency optimizers [8]. In this paper the machine will be optimally controlled with a model-based approach.

Harmonic iron and harmonic ohmic losses due to the pulsed converter output voltage can increase the induction machine losses [9]. Not only the direct losses due to the harmonics but also the change in the fundamental operating point due to the additional heating is important and influences the efficiency negatively [10].

In section 2, the 3LT<sup>2</sup>C losses and efficiency depending on the operating points of the machine are calculated and compared to the performance of the standard 2-level VSC. In section 3, the induction machine efficiency with loss minimizing control is considered. The difference of the harmonic losses caused by the 2-level and the 3-level converter is analyzed in section 4 and proved with detailed measurements on a machine test bench including a precise torque transducer. The impact of the efficiency optimal machine control on the converter operating points is analyzed and the total drive efficiency is calculated in section 5.

## 2 The 3LT<sup>2</sup>C prototype

### 2.1 Hardware setup

The 3LT<sup>2</sup>C prototype shown in Fig. 1 is implemented with 1200 V, 40 A IGBTs with antiparallel diodes (Infineon IKW40T120) forming the six-switch inverter circuit, and with 600 V, 50 A devices (Infineon IKW50N60T) for the three bidirectional switches between the phase outputs and the DC-link midpoint. Only 3 additional isolated gate drive power supplies are necessary compared to the simple 2-level converter because of the common emitter configuration of the 600 V IGBTs. Therefore, the costs can be kept low and are not comparable to the 3-level NPC topology.

The key performance data of the 3LT<sup>2</sup>C prototype is summarized in Tab. I. The converter is designed for an output power of 10 kW and allows the switching frequency to be set in a range from 4 – 24 kHz. At the nominal switching frequency of 8 kHz, the converter reaches a pure semiconductor efficiency of 99 %. Digital control, gate drive circuits and forced air cooling consume 20 W in addition. The power density is given with 3.3 kW/dm<sup>3</sup>.

An important feature is the ability to change the switching frequency during operation. Also the output voltage can be switched between 2-level and 3-level modulation during operation what allows an investigation of the impact on the induction machine efficiency without stopping the machine and changing its thermal conditions.

### 2.2 Switch commutation and modulation

The current commutation for each switching transition was considered in detail for the 3LT<sup>2</sup>C in [11]. Basically, the T-type bridge-leg can be connected to the positive, the neutral or the negative DC-link voltage level. During a switching transition the current naturally commutates to the correct branch if the 4 switches in a bridge leg are controlled in a way similar to the 3-level NPC converter [11]. It is not necessary to employ a current dependent commutation scheme, a simple turn-on delay is sufficient to guarantee reliable operation.

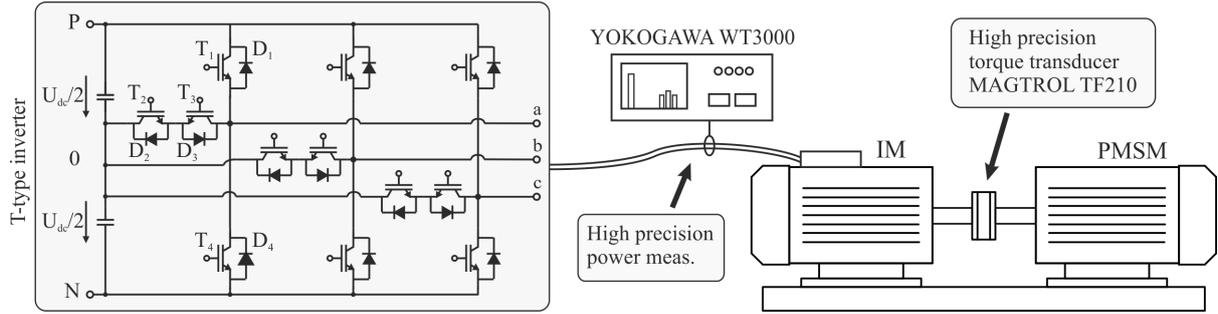


Figure 2: Schematic of the 3LT<sup>2</sup>C and the machine test bench. The graphical alignment of the top and bottom switches together with the two middle switches resemble the rotated character T. Therefore, the topology is referred to as T-type topology.

Table I: Parameters of the 3LT<sup>2</sup>C.

Parameter	Variable	Value
Nominal output power	$P_n$	10 kW
Nominal efficiency	$\eta_n$	99.0 %
Switching frequency	$f_s$	8 kHz
DC-link voltage	$U_{dc}$	650 V
DC-link capacitance	$C_{dc}$	$2 \times 240 \mu\text{F}$ in series
Volume	$V$	$3 \text{ dm}^3$
Weight	$m$	2 kg
Power density	$\rho$	$3.3 \text{ kW/dm}^3$
Power weight	$\sigma$	$5 \text{ kW/kg}$

The modulation strategies known from the 3-level NPC converter can also be applied to the 3LT<sup>2</sup>C. The modulation strategy is an important point for the converter efficiency [12]. A space vector modulation scheme with optimal clamping described in [13] is used for the prototype. A fixed optimal clamping scheme reducing the switching losses for an inductive current displacement of 30° is implemented which best fits the induction machine terminal behavior.

An issue related to the adopted optimal clamping space vector modulation is that the DC-link capacitors are loaded in an alternating fashion with three times the fundamental frequency. Therefore a certain DC-link voltage unbalance occurs periodically and has to be explicitly allowed. The space vector modulation is adopted to account for this voltage unbalance according to [14]. The compensation ensures that the output voltage is generated correctly although the link is not exactly balanced.

### 2.3 Converter losses and efficiency calculation

Especially the switching losses are considerably influenced by the combination of the two switch types in the same topology. The turn-on switching loss energy of the 1200 V IGBT decreases compared to a turn-on transition in the 2-level bridge-leg because the commutating diode is only rated for 600 V. Contrary, the 600 V IGBT turn-on loss energy increases as described in [11]. The switching loss energies have been measured with a T-type bridge-leg hardware setup for every switching transition.

In order to calculate the operating point dependent 3LT<sup>2</sup>C efficiency, an algorithm described in [15] was adopted. It is important that the optimal clamping is considered correctly in order to determine the switching and conduction losses over the phase-angle of the output voltage (cf. Fig. 3). Finally, the average losses in each semiconductor and the efficiency of the 3LT<sup>2</sup>C can be calculated.

Generally, the 3LT<sup>2</sup>C obtains very low losses for a wide range of switching frequencies. The conduction losses are comparable with the 2-level converter but the switching losses are further reduced because the commutation voltage is only half of the DC-link voltage  $U_{dc}/2$ . A detailed loss analysis and a comparison with the conventional 2-level and the 3-level NPC converter can be found in [11].

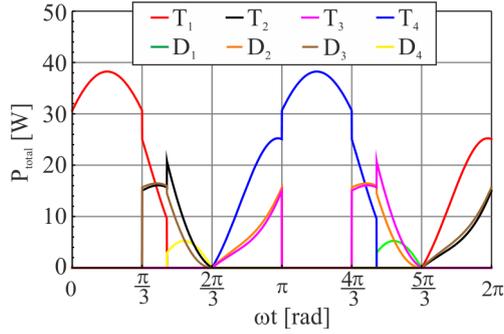


Figure 3: Semiconductor loss distribution over an output voltage period at  $\hat{U}_1 = 325$  V,  $\hat{I}_1 = 20.5$  A,  $\varphi_1 = 30^\circ$ ,  $f_s = 6$  kHz.

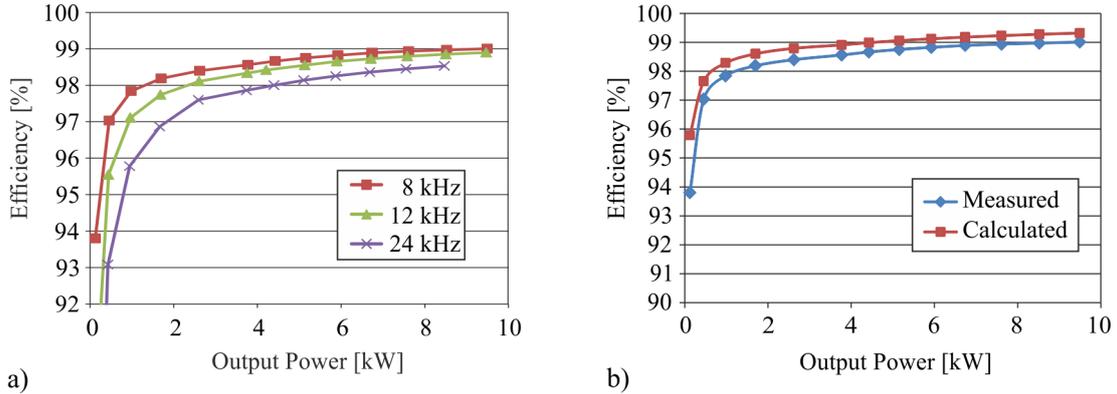


Figure 4: a) Measured efficiency of the 3LT<sup>2</sup>C prototype supplying a RL-load ( $R = 20 \Omega$ ,  $L = 2.5$  mH). b) Comparison between the calculated and the measured efficiency at  $f_s = 8$  kHz.

The efficiency of the 3LT<sup>2</sup>C has been measured with a Yokogawa WT3000 precision power analyzer (basic power accuracy of 0.02%). The measurements were conducted with a fixed RL-load and the output voltage was increased in small steps. The clamping interval was set to a symmetric clamping around  $0^\circ$  in order to match the small current displacement angle  $\varphi_1$  resulting from the mainly resistive RL-load. Fig. 4a) shows the measured efficiency over the output power for several switching frequencies. At a switching frequency of 8 kHz the converter reaches the target efficiency of 99%.

The efficiency calculation resulted in a slightly higher efficiency depicted in Fig. 4b). The deviation can be explained with the approximative linear models of the conduction and switching losses.

In a next step, the converter efficiency in the operating points given by the machine can be calculated. The operating points are dependent on the control of the machine and change with output torque and rotational speed. In the next section, the efficiency optimal control of the induction machine is presented, which has a major impact on the electrical operating point consisting of terminal voltage, current and current displacement angle. The influence on the converter efficiency is presented in section 5.

### 3 Model-based induction machine efficiency optimization

The induction machine efficiency is highly dependent on the operating point. Conventionally, the machine is controlled with constant rotor flux (corresponding to constant u/f control) and therefore is overmagnetized for most of the operating points with low torque. The related ohmic and iron losses are high and reduce the machine efficiency. To overcome this drawback, several optimized control methods have been proposed over the last decades. Basically they can be divided into search algorithms where a control variable (i.e. the rotor flux) is altered periodically to find the operating point with minimum losses, and model-based optimizers which directly adjust the rotor flux dependent on the operating point.

In this paper a model-based optimizer is implemented which adapts the rotor flux in order to



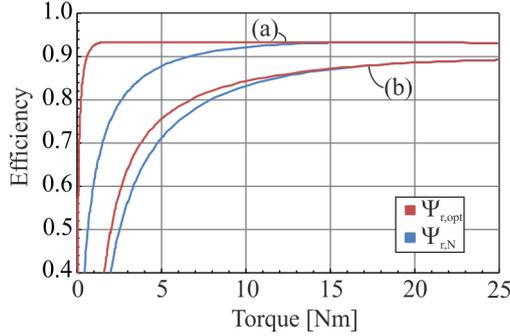


Figure 6: Induction machine efficiency with nominal and optimal rotor flux at nominal speed. a) Pure electrical efficiency, b) friction and windage losses included.

The rotor leakage inductance can be neglected if it is small and the optimal rotor flux reduces to

$$\Psi_{r,\text{opt}} \approx \sqrt{\frac{2}{3p}} \cdot \left[ \frac{L_m^2 R_c (R_r + R_s)}{p^2 L_m^2 \omega_R^2 + R_c R_s} \right]^{\frac{1}{4}} \cdot \sqrt{T_e}. \quad (3)$$

The equivalent core loss resistance  $R_c$  is dependent on the electrical frequency. As an approximation it can be scaled linearly with the rotor speed (cf. [17]) because the slip speed is small for controlled inverter operation. A temperature corrected value of the rotor resistance  $R_r$  could be used for the computation of the optimal flux reference due to its distinct temperature dependency.

The optimal rotor flux usually is lower than the rated flux for low torque operation and higher than the rated value for low speed and high torque operation. The minimum and maximum rotor flux are constrained to 30% and 100% of the rated flux in order to keep a good dynamic response and not to saturate the machine.

The induction machine efficiency can be increased in a wide operating range. Curve set (a) in Fig. 6 shows the achievable pure electric efficiency with the optimal rotor flux. The efficiency can be maintained on a high level also for low torque operation. Unfortunately all conventional machines suffer from additional mechanical losses. The machine efficiency is decreased due to friction and windage. Due to the shaft mounted fan of the self-cooled induction machine these loss components are considerably high ( $P_{\text{fric},n} = 285 \text{ W}$ ) and can not be neglected. For the machine used in the test bench the mechanical losses have been measured and approximated with a least squares curve fit.

$$P_{\text{fric}} = 0.0283 \cdot \omega_R^{1.613} \quad (4)$$

Curve set (b) in Fig. 6 shows the achievable efficiency including the mechanical losses. It is still possible to increase the efficiency by several percent for low torque operation if the optimal rotor flux is used.

The terminal behavior of the machine changes considerably if operated with the optimal rotor flux. In the low torque region the supplied voltage and the current decrease. Also the voltage to current displacement angle reduces from an inductive behavior to about  $40^\circ$ . This has again an impact on the converter efficiency what is discussed in section 5.

## 4 Comparison of the harmonic machine losses

The harmonic machine losses due to the pulsed output voltage depend on many manufacturing specific machine parameters such as lamination thickness, winding type, slotting and are difficult to calculate and to measure. Depending on the switching frequency, the harmonic losses consist not only of ohmic but also of iron losses. A promising approach to estimate the influence of different topologies and modulation schemes is the harmonic loss factor curve [18]. Basically, a characteristic loss factor curve is determined by measurement which describes the harmonic losses depending on the supplied voltage frequency spectrum.

In this paper a similar approach was chosen. The harmonic losses of the machine have been measured for several switching frequencies and for 2-level and 3-level modulation. To measure

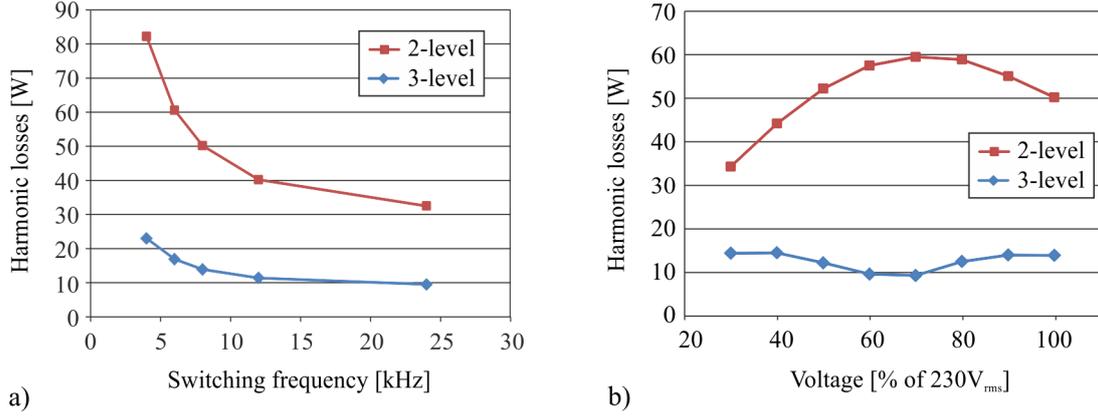


Figure 7: a) Measured harmonic machine losses as a function of the switching frequency ( $U_s = 230$  V). b) Harmonic machine losses as a function of the output voltage ( $f_s = 8$  kHz).

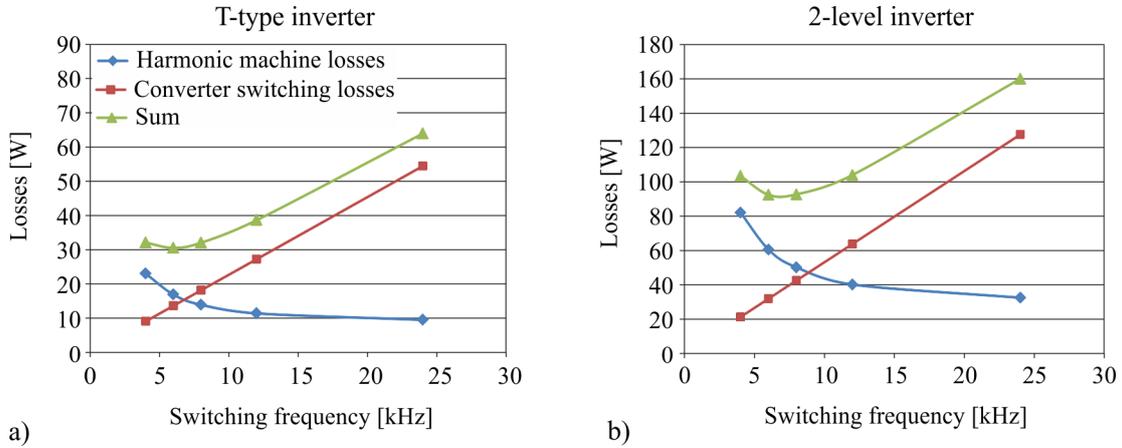


Figure 8: Optimization of the switching frequency dependent loss components with a) the 3LT<sup>2</sup>C and b) the 2-level converter. The harmonic machine losses are measured during nearly no-load condition and the switching losses are calculated for nominal operation ( $f_s = 8$  kHz).

the harmonic losses, the precision power analyzer Yokogawa WT3000 was configured in such a way that it automatically subtracts the fundamental active power component from the total measured active power. Therefore, an accurate measurement of the harmonic losses is ensured and no postprocessing is necessary.

The harmonic loss curves for several frequencies and for 2-level and 3-level modulation are depicted in Fig. 7a). The measurements were taken under quasi no-load conditions, therefore only friction and windage losses of the induction machine and the PMSM load machine were applied. It can be seen that the harmonic losses decrease for higher switching frequencies but stagnate on a certain value.

The harmonic machine losses with 3-level modulation are approximately reduced by a factor of 4 compared to the 2-level modulation. This can be explained theoretically with the output voltage spectrum and is analyzed in detail in [19]. There, also the exact relationship between the harmonic losses and the modulation index for 2-level and 3-level modulation is given. The measurements depicted in Fig. 7b) confirm the predictions of [19] precisely. The harmonic losses for 2-level modulation have a maximum at 70% of the rated voltage and the 3-level modulation has two smaller peaks at 30% and 90% of the rated voltage.

## 5 Total system efficiency

The total system efficiency depends on the converter losses, the fundamental and the harmonic machine losses. The efficiency optimal control of the induction machine takes influence on the converter operating point and also on the harmonic machine losses due to a changed modulation index. The switching frequency has an impact on the converter losses and on the machine har-

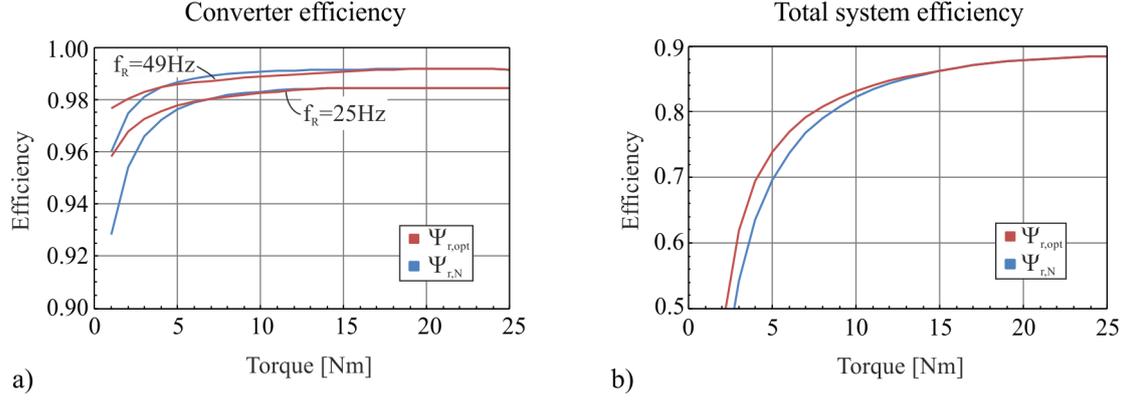


Figure 9: a) Calculated 3LT<sup>2</sup>C efficiency in the machine operating points for nominal and optimal rotor flux ( $f_s = 8\text{ kHz}$ ). b) Calculated total drive efficiency including harmonic machine losses ( $f_s = 8\text{ kHz}$ ).

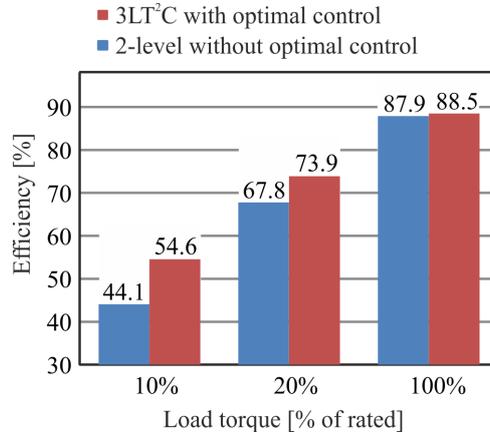


Figure 10: Comparison of the total drive efficiency between a standard 2-level converter with constant rotor flux control and the 3LT<sup>2</sup>C with optimal rotor flux control ( $f_s = 8\text{ kHz}$ ).

monic losses. Therefore, a system optimized for efficiency has to consider the optimal switching frequency and the optimal operating point.

In order to find the optimal switching frequency, which results in minimum losses of the total system, the harmonic machine losses, the converter switching losses, and their sum are depicted in Fig. 8a) for the 3LT<sup>2</sup>C and in Fig. 8b) for the 2-level converter. The switching losses have been calculated for rated speed and torque. The harmonic machine losses have been measured at no-load condition. Although there are some variations of the harmonic machine losses under loaded conditions [10] they are assumed to be small and are neglected. The minimum of the total switching frequency dependent losses is at  $f_s = 6\text{ kHz}$  for the 3LT<sup>2</sup>C and at  $f_s = 7\text{ kHz}$  for the 2-level converter. A higher switching frequency therefore does not reduce the total losses and should only be chosen if necessary because of the dynamic performance or the acoustic noise reduction.

Although this approach allows no statements for the optimal switching frequency of an arbitrary machine and converter combination the results will potentially be similar for comparable machines and power ratings.

The optimal machine operating point influences the fundamental machine losses and the converter losses. In order to find the global minimum, both subsystems have to be considered and should be optimized at the same time. For low voltage applications, the machine losses are clearly dominant and the optimal operating point achieved by the machine efficiency optimization alone is very close to the total system optimal operating point. It is therefore sufficient to use the optimal rotor flux reference in order to optimize the total system efficiency.

The converter efficiency in the operating points required by the machine is depicted in Fig. 9a) for control with nominal and optimal rotor flux. If the torque is below 20% of rated, the optimal rotor flux has a positive impact on the converter efficiency. This is mainly due to a reduced voltage to current displacement angle. For higher torques, the 3LT<sup>2</sup>C efficiency is very high and remains in the range from 98% to 99%.

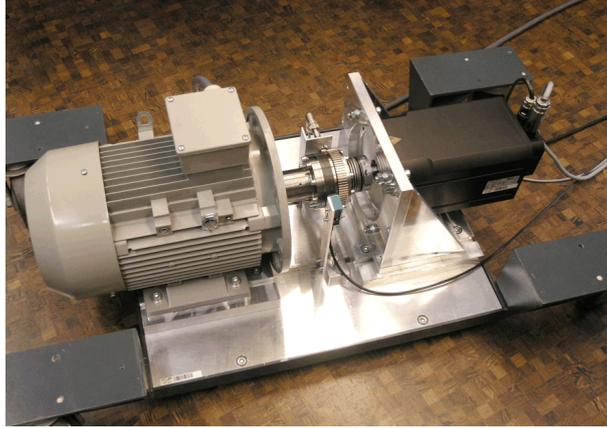


Figure 11: Machine test bench with an induction machine and the PMSM load machine. Torque and speed are measured with a wireless torque transducer (Magtrol TF 210).

The total system efficiency is depicted in Fig. 9b). Due to the very low losses of the 3LT<sup>2</sup>C the total drive efficiency is mainly determined by the machine. With the optimal rotor flux the efficiency can be increased in the low torque region ( $T_e < 20\% \cdot T_n$ ) by more than 6%. Naturally, the absolute energy savings are considerably lower in this low torque region because the processed power is also decreased. If these absolute losses which can be saved are compared to the nominal machine power an equivalent efficiency increase of approximately 1.2% can be achieved.

Finally, the conventional drive system consisting of a 2-level converter with a standard machine control algorithm (constant u/f) can be compared to the optimized drive system with the new 3LT<sup>2</sup>C (cf. Fig. 10). In the low torque region ( $T_e < 20\% \cdot T_n$ ) the efficiency can be increased by more than 6% by the use of the optimal flux control and even for rated load, the efficiency can be increased by 0.6% because of the reduced harmonic machine losses and the higher efficiency of the 3LT<sup>2</sup>C. The machine will obtain only small additional heating by the harmonic losses and the insulation stress is reduced because of the 3-level output voltage waveform.

## 6 Hardware setup

The measurements have been accomplished with a 99% efficient 10 kW prototype of the 3LT<sup>2</sup>C depicted in Fig. 1 and a standard 7.5 kW induction machine (ecoDrives ACA 132 SB-2/HE). The induction machine is mounted on a test bench and coupled with a highly precise torque transducer (Magtrol TF 210, 0.05% accuracy) to a PMSM load machine (LST-158-4-30-560) shown in Fig. 11. The losses are measured directly with the high precision power analyzer Yokogawa WT3000. It is able to directly calculate the fundamental and harmonic loss components and therefore no additional post processing is necessary.

## 7 Conclusion

In this paper a 3-phase 3-level T-type converter topology for high efficiency low voltage drive applications is presented. The drive system is competitive to standard 2-level solutions because of the relatively low cost 3LT<sup>2</sup>C. The total system efficiency is optimized considering fundamental and harmonic losses in the converter and in the machine. A total efficiency increase of more than 6% in the low torque range was achieved. The proposed variable speed drive is tested with a hardware prototype and a machine test bench including high precision power and torque measurements.

Employing new semiconductor devices such as SiC-diodes will not lead to a significant efficiency increase of the total drive system because the harmonic machine losses and the switching losses are already on a very low level with the 3LT<sup>2</sup>C. The only way to further increase the total drive efficiency is reducing the conduction losses of the switches or reducing the mechanical, ohmic and iron losses of the machine.

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